



Docket No.: M4065.0941/P941  
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:  
Chandra Mouli

Application No.: 10/612,974

Filed: July 7, 2003

Art Unit: N/A

For: PIXEL WITH STRAINED SILICON  
LAYER FOR IMPROVING CARRIER  
MOBILITY AND BLUE RESPONSE IN  
IMAGERS

Examiner: Not Yet Assigned

**INFORMATION DISCLOSURE STATEMENT (IDS)**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 CFR 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is filed within three months of the U.S. filing date (37 CFR 1.97(b)(1)).

A copy of each reference on PTO/SB/08 is attached.

In accordance with 37 CFR 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR 1.56(a) exists. In accordance with 37 CFR 1.97(h), the filing of this Information Disclosure statement shall not be construed to be an admission that any patent, publication or other information referred to therein is "prior art" for this invention unless specifically designated as such.

It is submitted that the Information Disclosure Statement is in compliance with 37 CFR 1.98 and the Examiner is respectfully requested to consider the listed references.

The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 04-1073, under Order No. M4065.0941/P941. A duplicate copy of this paper is enclosed.

Dated: October 7, 2003

Respectfully submitted,

By 

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PTO/SB/08a/b (08-03)

Approved for use through 07/31/2006. OMB 0651-0031

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Substitute for form 1449A/B/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)				<b>Complete if Known</b>	
				Application Number	10/612,974
				Filing Date	July 7, 2003
				First Named Inventor	Chandra Mouli
				Art Unit	N/A
				Examiner Name	Not Yet Assigned
				Attorney Docket Number	M4065.0941/P941
Sheet	1	of	1		

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			

FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)					

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NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		
	CA	ILIANA L. FUJIMORI et al., "A 256x256 CMOS DIFFERENTIAL PASSIVE PIXEL IMAGER WITH FPN REDUCTION TECHNIQUES," <i>IEEE Journal of Solid-Stat eCircuits</i> , Vol. 35, No.12, December 2000.		
	CB	PETER SINGER, "Strained Silicon Ready for Prime Time," <i>Semiconductor International</i> , 11/1/2002.		

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